PROCESSING CIRCUIT CAPABLE OF MODIFYING DIGITAL AUDIO SIGNALS

Abstract

The present invention provides an audio processing circuit for receiving a first stream complying with a first standard (for example. MPEG audio standard) and generating a second stream complying with a second standard which is a digital interface standard (for example, S/PDIF standard). The audio processing circuit includes:a stream buffer for storing a plurality of frames of the first stream;a stream recovering circuit for detecting at least one of a plurality of fields in the frames, modifying at least one of the plurality of fields according to the first standard, and generating modified frames; a first buffer for storing the modified frames; and a burst circuit for partitioning the modified frames into a plurality of payload sections, adding a preamble to each of the payload sections, and forming the second stream.